

110V
60Hz

LINE FILTER
PLUG

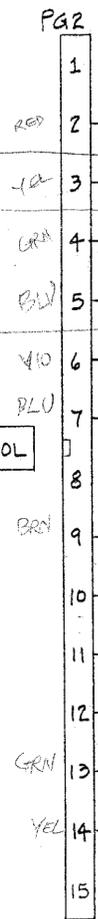
MONITOR
OUTPUTS ARE
DIVIDE BY 100

Front panel

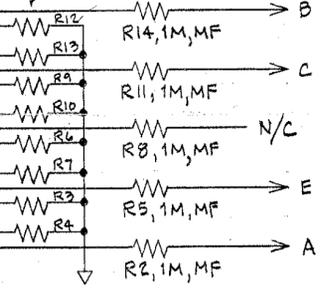
POSITIVE
OFFSET

NEGATIVE

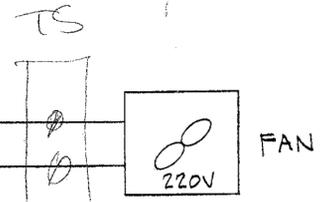
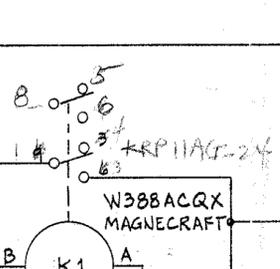
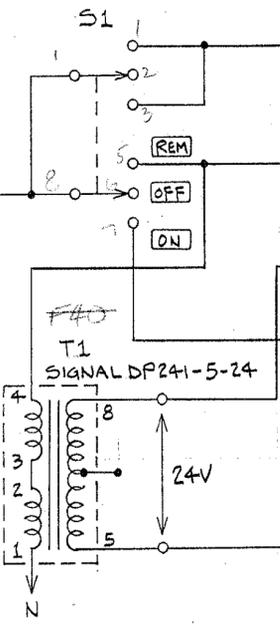
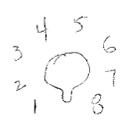
SLOW CONTROL



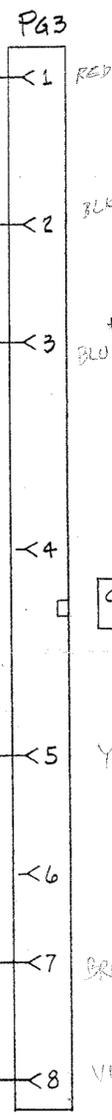
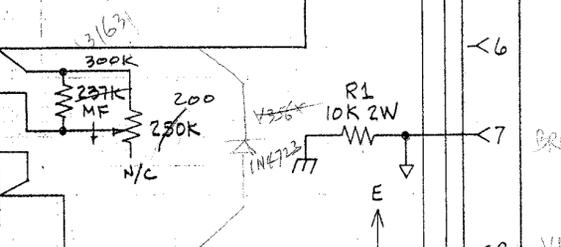
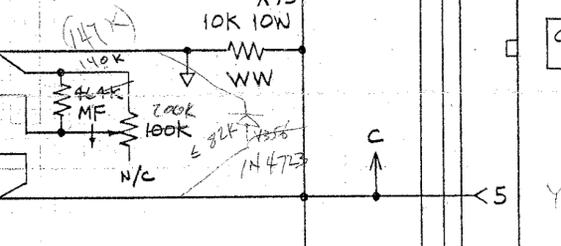
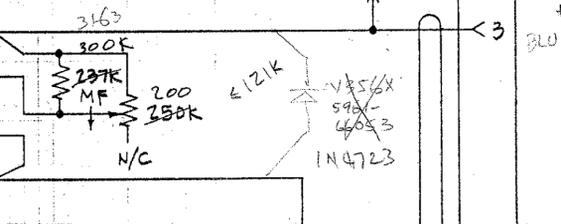
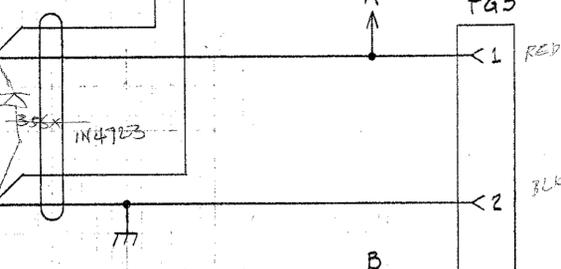
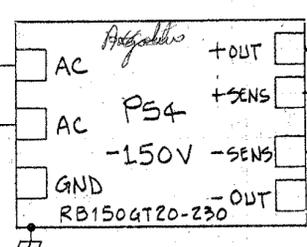
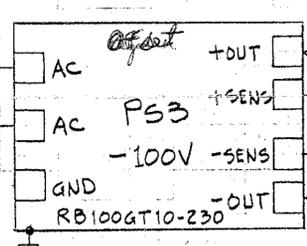
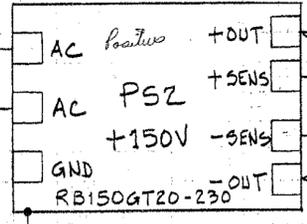
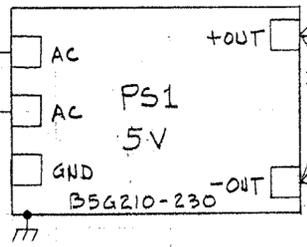
(IN PARALLEL
4 PLACES)
11K & 121K
1/4W MF



15 PIN
DA15
REAR
PANEL



ELDEMA
CF-B-2181
6.3V @ 200mA
LAMP HOLDER
6250-42503
LEDs 6250-42509 GRN



8 PIN
AMP CPC
206433-1
R.P.

REAR
PANEL

14V 876

I:				ALEPH GATING DC POWER SYSTEM			
II:				SCHEMATIC			
III:				LAWRENCE BERKELEY LABORATORY UNIVERSITY OF CALIFORNIA			
SHOWN ON	ACCT & SER	DATE ISSUED	NO. REQD.	DRWN. BY	CHKD. BY	DATE	NO.
				MP	MP	1-14-87	21X9393-S1 A
CHG. LTR. BY	DRAWN BY	CHECKED BY	DATE	CHANGES	DATE RECD.	DEL. TO	ENG.
	MB		7/22/87	MOVED LT1 & MISC. CHANGES			PAUL SALEZ