

DIGITAL		J7 J80PRA		DIGITAL	
-5V	1			2	-5V
+6V	3			4	+6V
VIN-BIAS-SB	5			6	SHAPER0B
SHAPER1B	7			8	SHAPER2B
SHAPER3B	9			10	SHAPER4B
SHAPER5B	11			12	SHAPER6B
SHAPER7B	13			14	SHAPER8B
SHAPER9B	15			16	SHAPER10B
SHAPER11B	17			18	SHAPER12B
SHAPER13B	19			20	SHAPER14B
SHAPER15B	21			22	SHAPER16B
SHAPER17B	23			24	SHAPER18B
SHAPER19B	25			26	SHAPER20B
SHAPER21B	27			28	SHAPER22B
SHAPER23B	29			30	SHAPER24B
SHAPER25B	31			32	SHAPER26B
SHAPER27B	33			34	SHAPER28B
SHAPER29B	35			36	SHAPER30B
SHAPER31B	37			38	SHAPER32B
SHAPER33B	39			40	SHAPER34B
SHAPER35B	41			42	SHAPER36B
SHAPER37B	43			44	SHAPER38B
SHAPER39B	45			46	SHAPER40B
SHAPER41B	47			48	SHAPER42B
SHAPER43B	49			50	SHAPER44B
SHAPER45B	51			52	SHAPER46B
SHAPER47B	53			54	SHAPER48B
SHAPER49B	55			56	SHAPER50B
SHAPER51B	57			58	SHAPER52B
SHAPER53B	59			60	SHAPER54B
SHAPER55B	61			62	SHAPER56B
SHAPER57B	63			64	SHAPER58B
SHAPER59B	65			66	RT1-2
RT1-1	67			68	ENABLEN-B
SHIFT-CLOCK-B	69			70	LEFT-DATA-B
VREF	71			72	CAL-GATEN
CAL-GATEP	73			74	NODE-GND
NODE-6	75			76	NODE-5
NODE-4	77			78	INTERLOCK-B
	79			80	

D I S T	I:	TITLE EOS DETECTOR ELECTRONICS				
	II:	DETECTOR ELECTRONICS BOARD				
	III:	TAXI CONTROL LOGIC				
SHOWN ON	SIDE B - CONNECTORS ONLY					
ACCOUNT NUMBER	DRAWN STIRKKINEN	DATE 9/21/90	LAWRENCE BERKELEY LABORATORY			
SERIAL NUMBER	CHECKED	DATE	UNIVERSITY OF CALIFORNIA			
DATE ISSUED	NO. RECD.	APPROVED	DATE	OFFICE OF ELECTRONICS ENGINEERING		
DATE RECD.		ENGINEER F. BIESER M. NAKAMURA, R. JONES	FILE NO. C011S12	SIZE C	DRAWING NO. 14V8754 S6	
DEL. TO	SCALE NONE	/JOBS/C011			REV. A	
					SHEET 6 OF 6	